

## INSTALLATION

Versions of the Logica XT with a 16-pin connector require a 5V rail in your case, those with a 10-pin connector do not.

Ensure that there is enough power left to supply this module.

Beware of the orientation; the stripe on the ribbon cable should match a similar stripe for the -12 (minus 12 Volts) indication on your supply board connector.

Supply rail	Current draw (16-pin)	Current draw (10-pin)
+12V	0 mA	21 mA
-12V	1 mA	1 mA
+5V	16 mA	0 mA

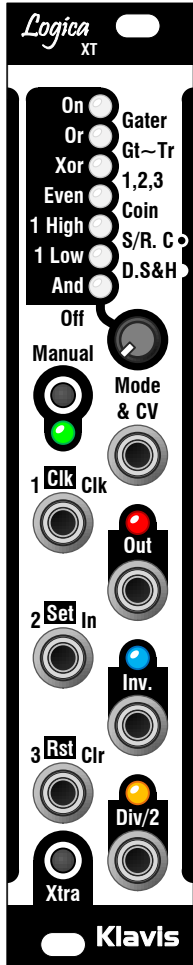
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## SUPPORT & ADDITIONAL INFO

The complete users manual is available on:  
[www.klavis.com/support](http://www.klavis.com/support)

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## USAGE & PANEL

Two sets of functions are offered: simple (left column), and advanced (right column). Switching between sets is done by a quick press of Xtra. A function is selected by the sum of the knob position and the voltage present at the Mode CV jack. Inv out is always the reverse of Out. Div/2 toggles every time Out goes high.

You can bring 4 signals (3 jacks and a push-button). Unused jacks are normalized or ignored as needed.

With S/R.C and D.S&H functions, the input jacks have a specific role indicated respectively in the middle and on the right side of each jack.

When the knob+CV setting is full up or full down, Out is forced high or low respectively.

The default setting of the Manual button and various advanced timing settings can be modified and stored.

See the manual for details.

## FUNCTIONS

Function	Out jack
On	Forced to On
Or	On if at least 1 input is on
Xor	On if 1 or more inputs are on but not all
Even	On if the number of inputs being on is even
1 High	On if only input is high
1 Low	On if only 1 input is low
And	On when all inputs are on
Gater	Combines gates and creates adjustable retrigger
Gt~Tr	Gate to trigger (or Trigger to Gate) with adjustable duration
1, 2, 3	Input sequence validation (safe locker)
Coin	Random draw with input-related weight (2, 4, 8)
S/R.C	Bistable latch with Set, Reset and Clock control
D.S&H	Digital Sample and Hold with adjustable delay*
Off	Forced to Off

\* over 5000 steps memory